

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S76	2	"6360340".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 10:02
S77	995	((memory with (map\$1 image) with comparison) and test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 10:06
S78	5	((memory with (map\$1 image) with comparison) and (parametric with test))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 10:06
S79	209	((memory with (map\$1) with comparison) and test)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 10:08
S80	5	S79 and parametric	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 10:07
S81	2545	memory with cell with redundant with address\$2	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 15:14
S82	0	memory with cell with redundant with address\$2 with parametric	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 15:14
S83	80	memory with cell with redundant with address\$2 with fault	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 15:50

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S84	2	"20050138495"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/02/27 15:50
S85	33066	(memory dram sram) with (control adj system)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/07 11:36
S86	609	(memory dram sram) with (control adj system) with (embeded integrated (on adj chip))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/07 11:39
S87	0	(memory dram sram) with (control adj system) with (embeded)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/07 11:39
S88	609	(memory dram sram) with (control adj system) with (integrated)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/07 11:39